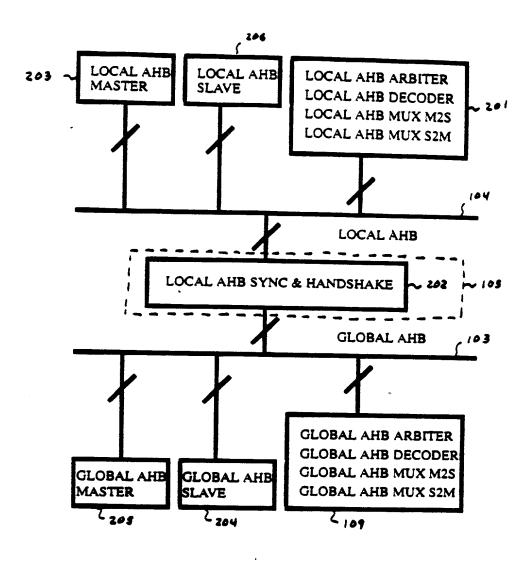
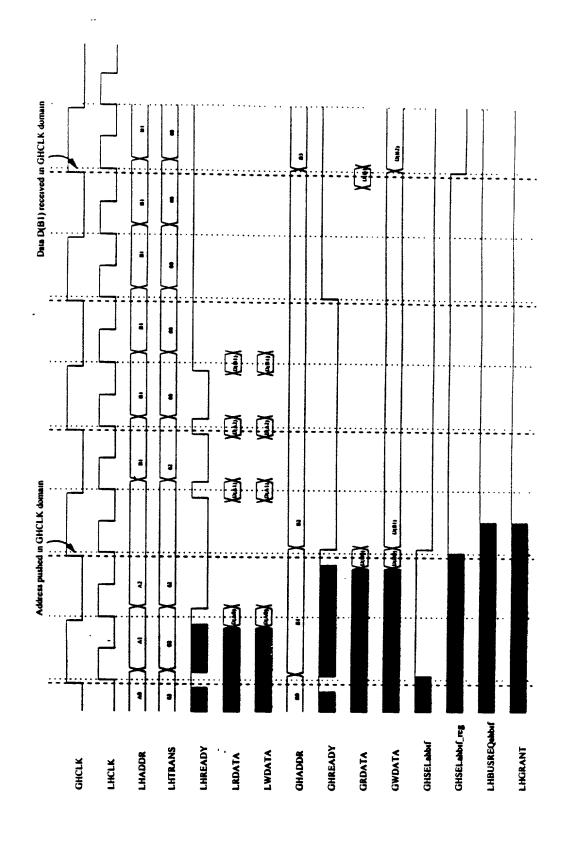
100

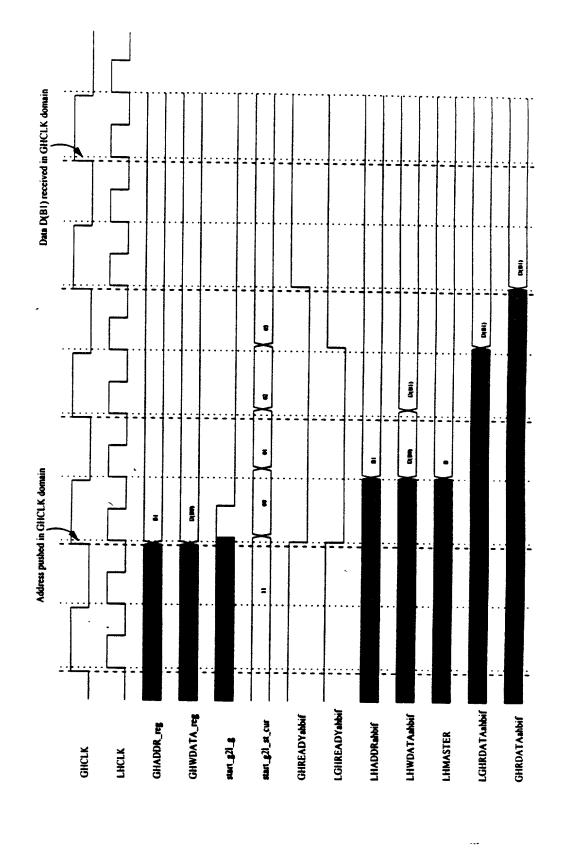
FIG. 1



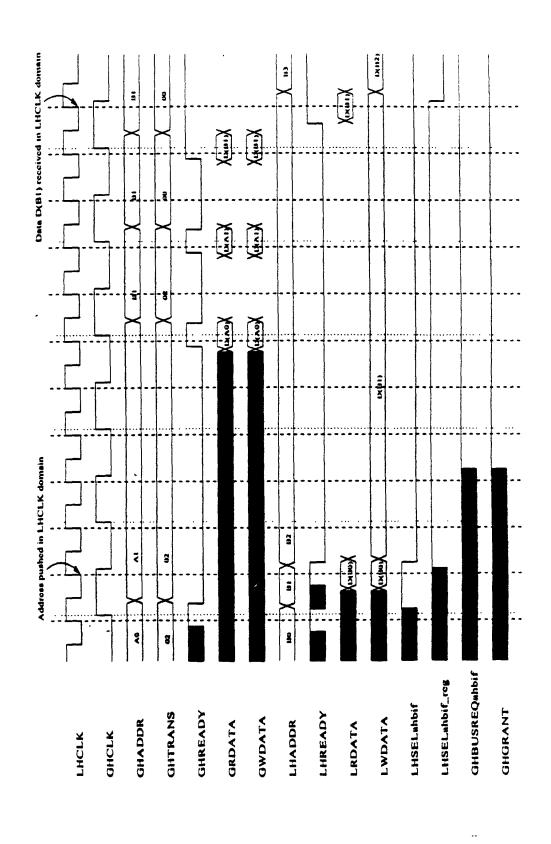


F16. 28

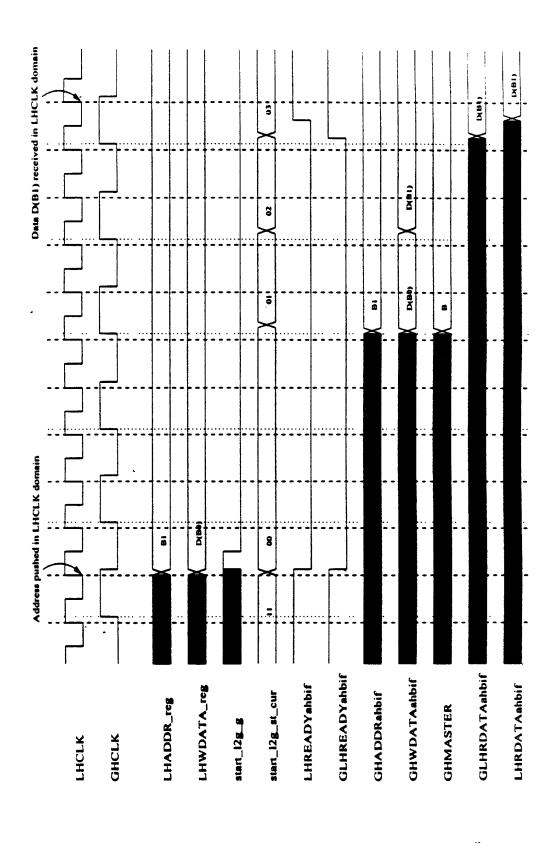
ı



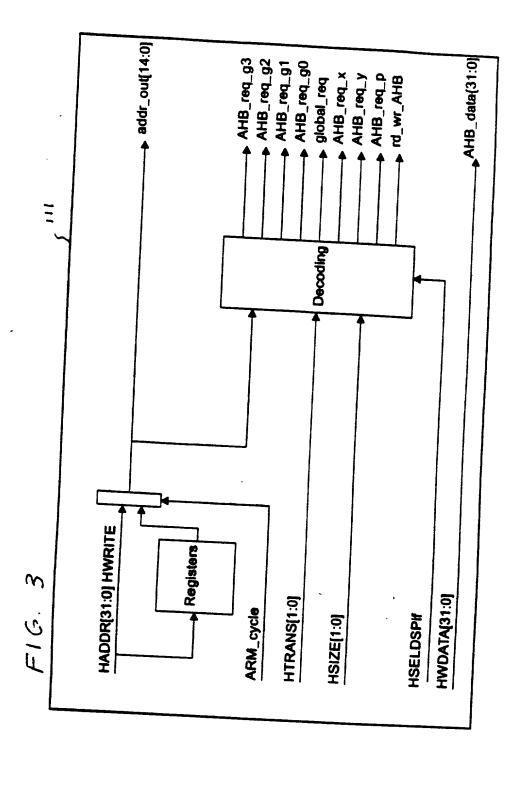
F16 20



1



10. CME



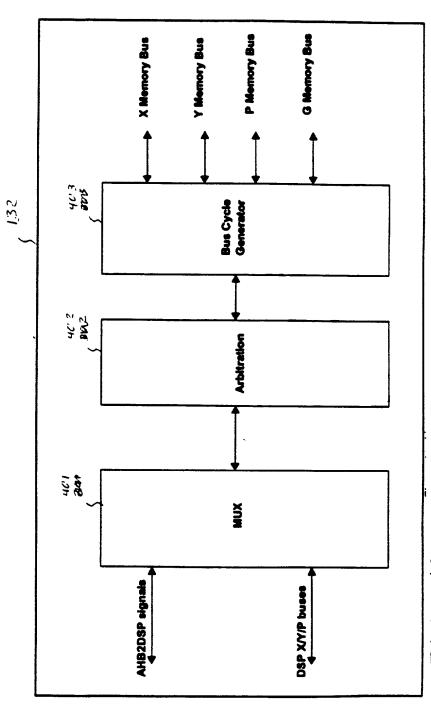
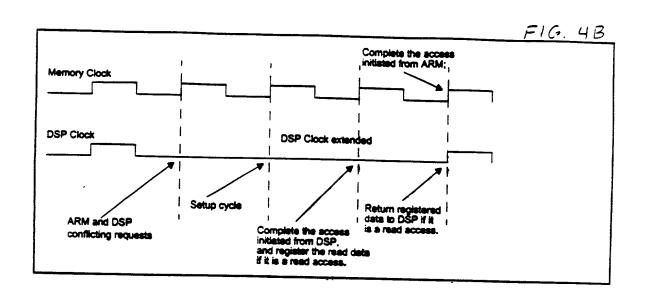
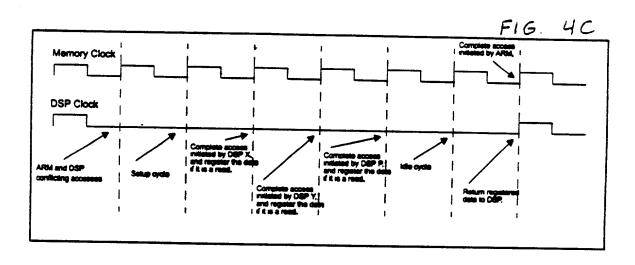
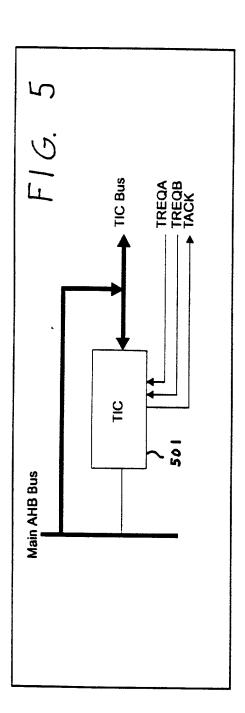
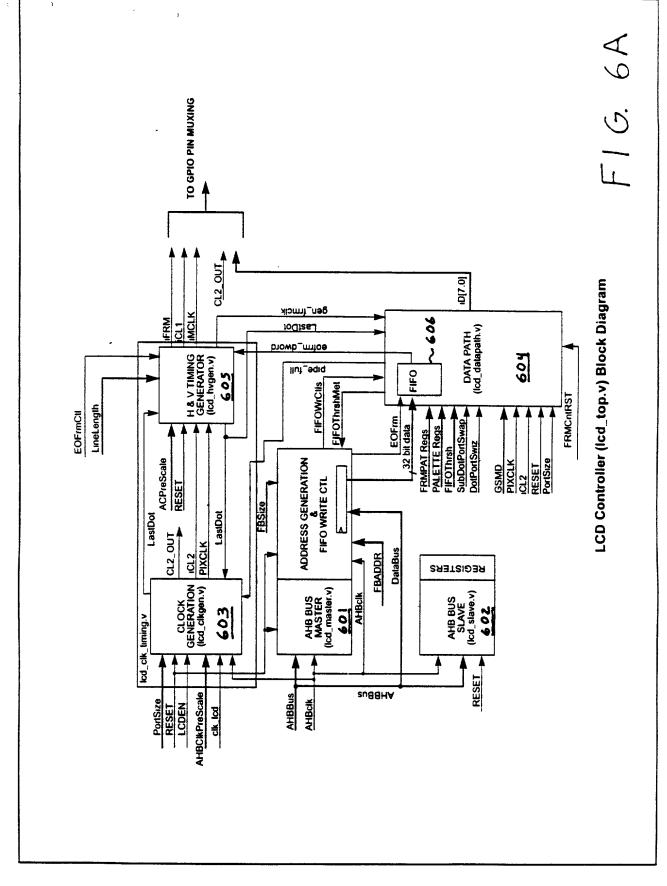


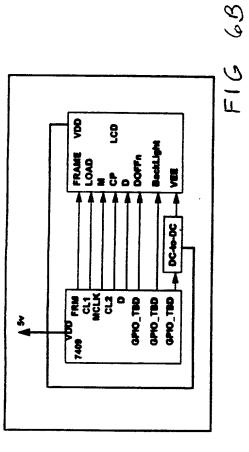
FIG. 4A

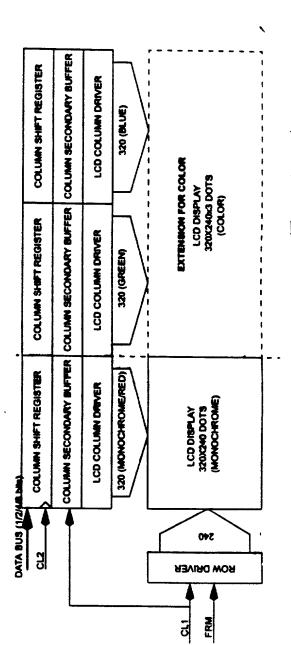






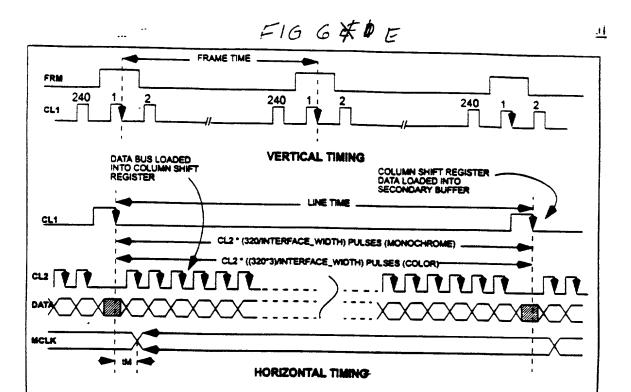


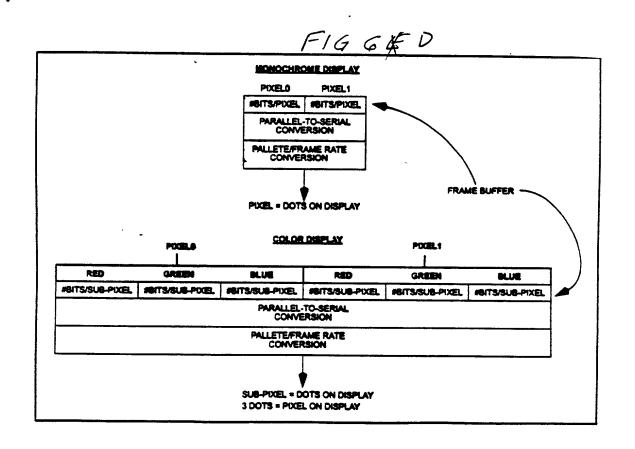


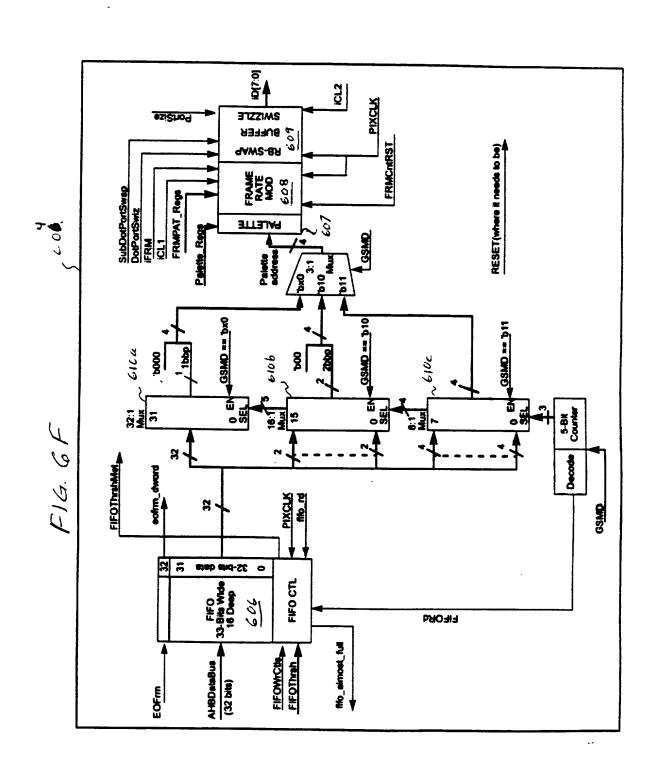


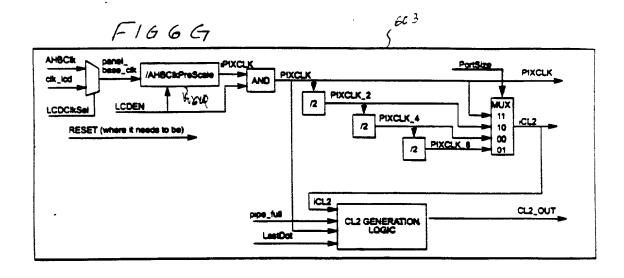
F16.6C

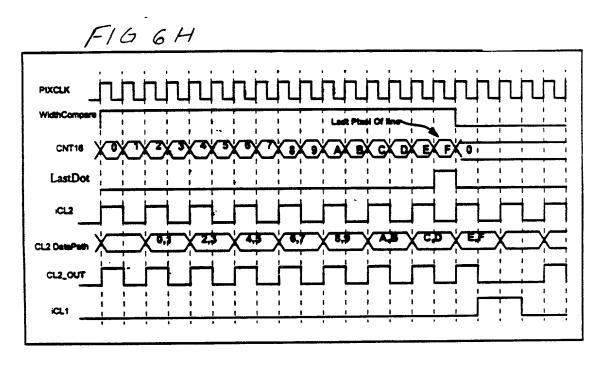
ļ.i

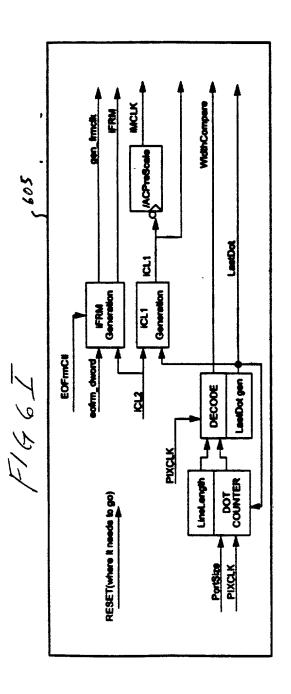


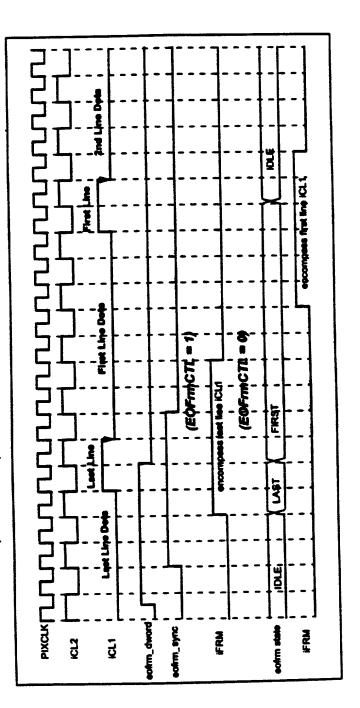




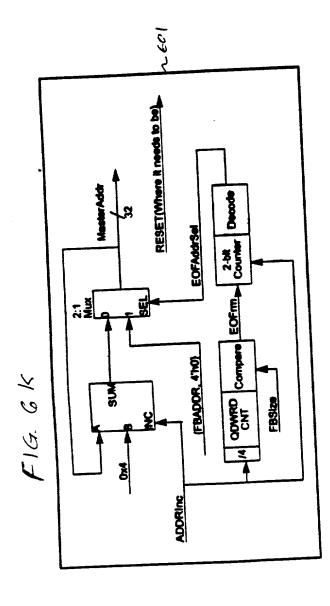


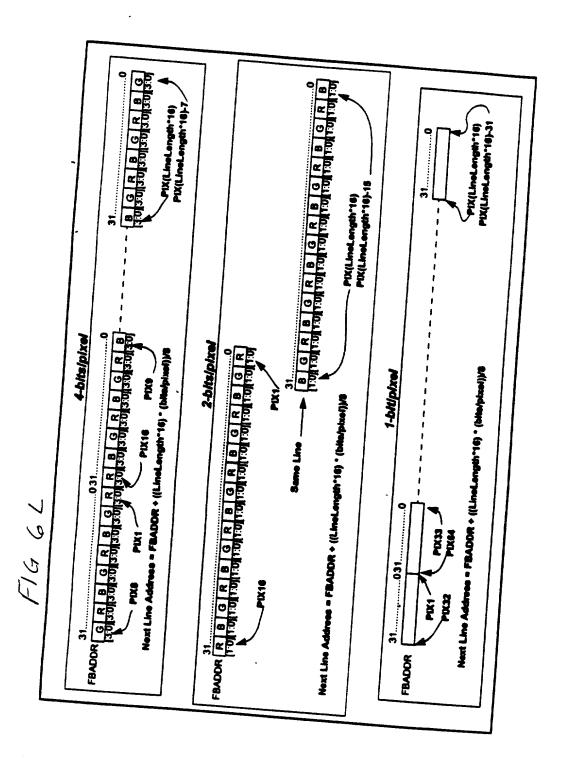


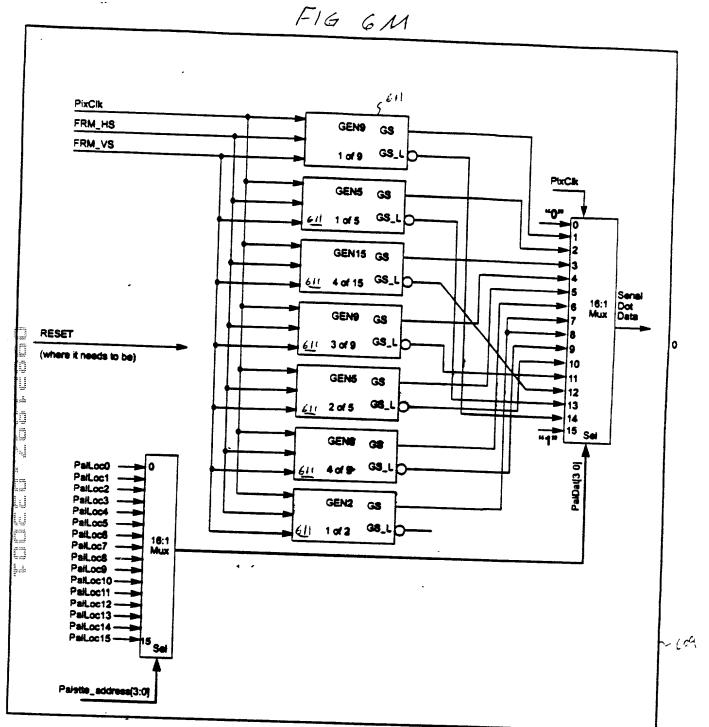


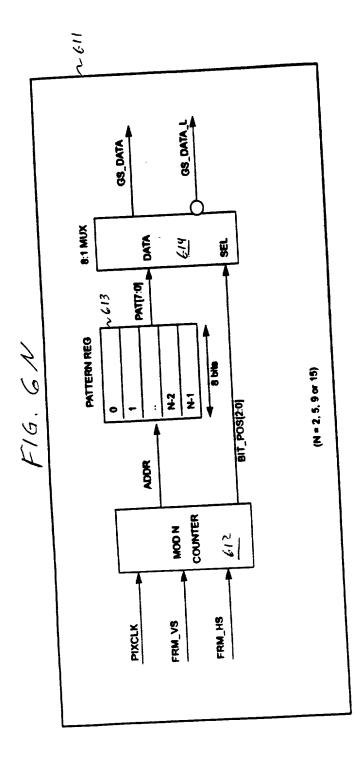


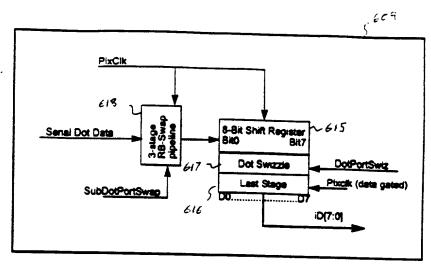
F1667











F1960

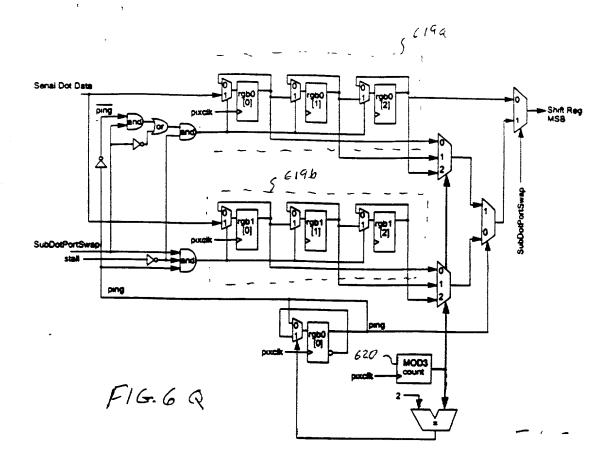
F16.6P

Time (from the shift register point of view)

SubDotPortSwap = 0

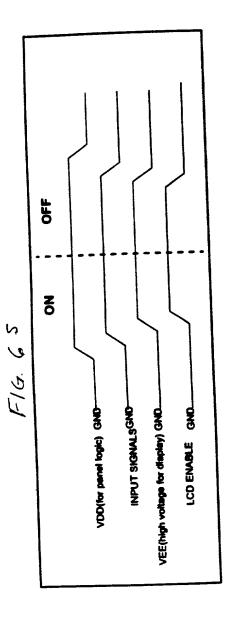
{BnGnRn}.....{B3G3R3}{B2G2R2}{B1G1R1}{B0G0R0}

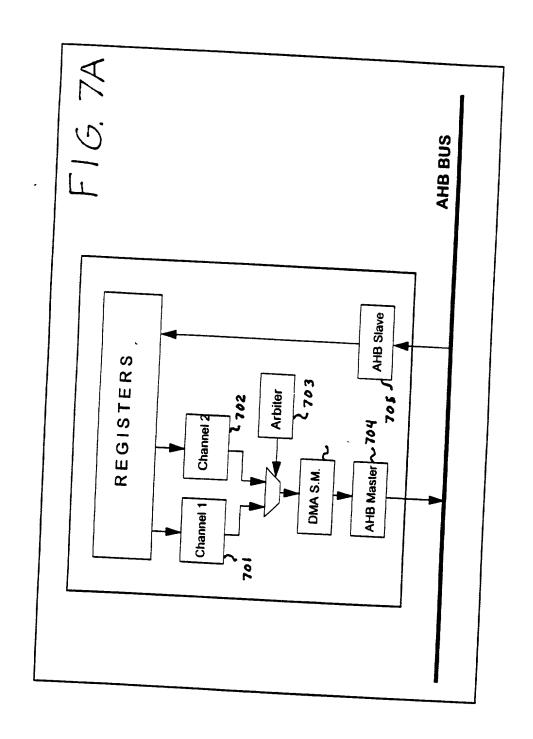
SubDotPortSwap = 1

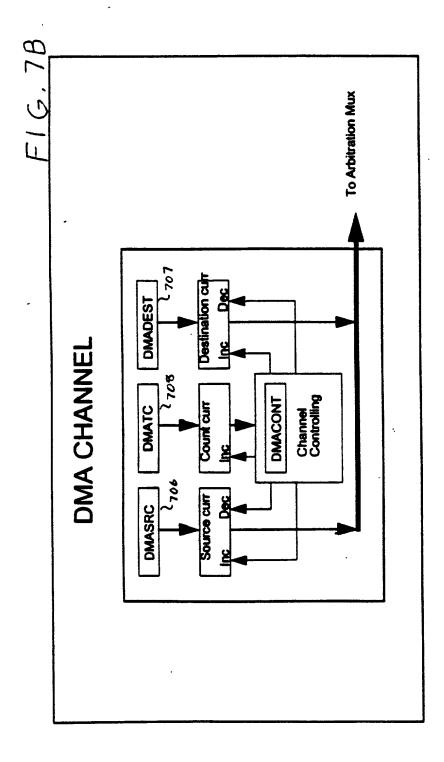


F1G 6.R

DotPortSwiz = 0									DotPortSwiz = 1											
SHFT REG LSB				SHFT REG MS8				MSB	SHFT REG LSB					SHFT REG MS8						
	<u>*</u>							•	_		1								¥	
DOTE	7	6	5	4	3	2	1	0]	роти	7	1	:	3	4	3	2	1	0	
DATA BUS BITS	0	1	2	3	4	5	6	7]	DATA BUS SITS	7	6		1	4	3	2	1	0	

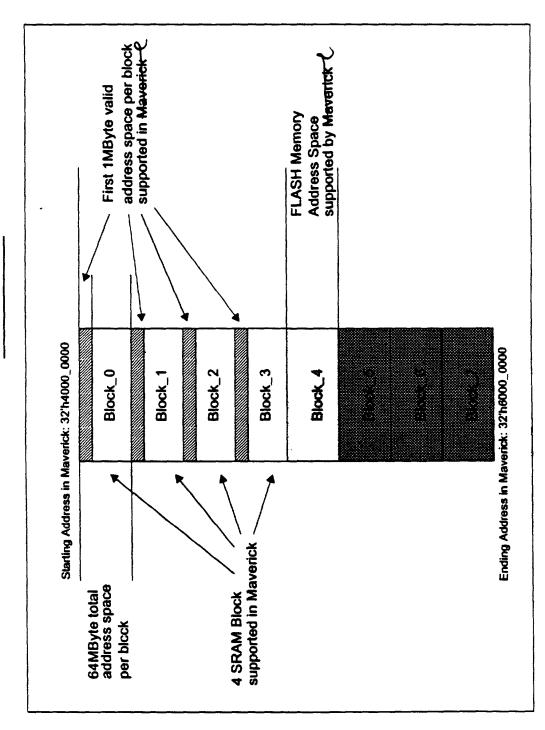


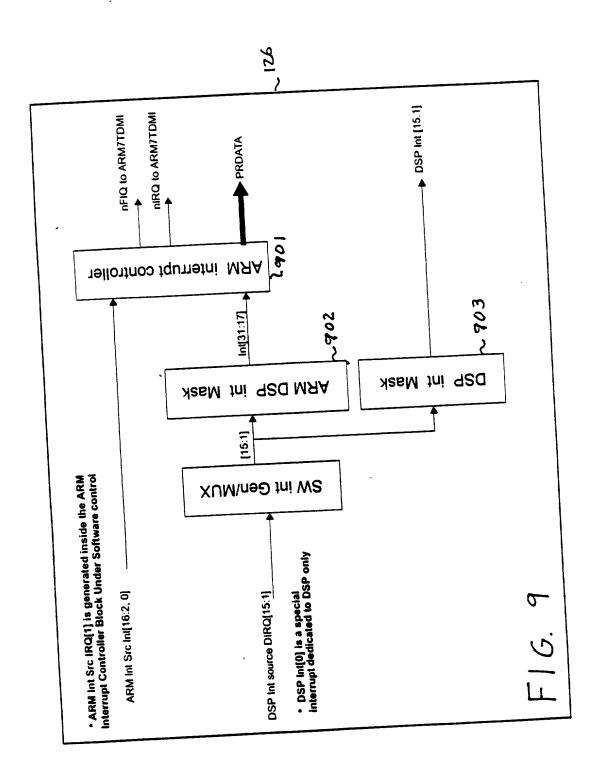


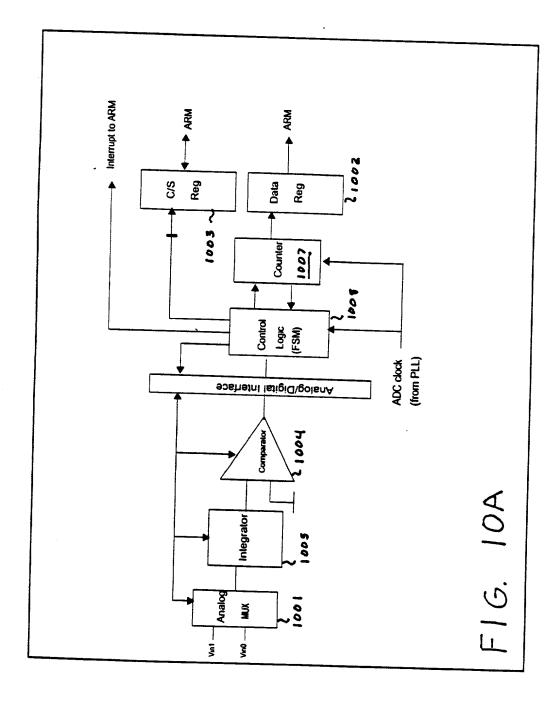


₹

F16. 8







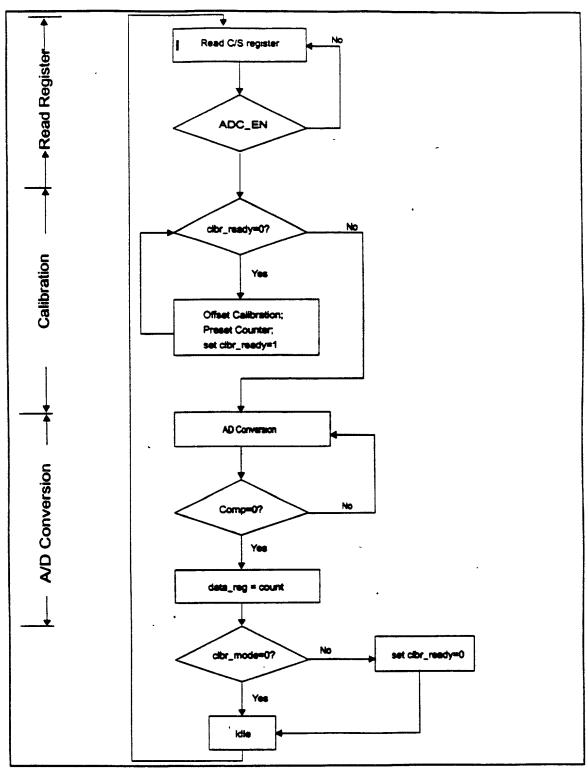
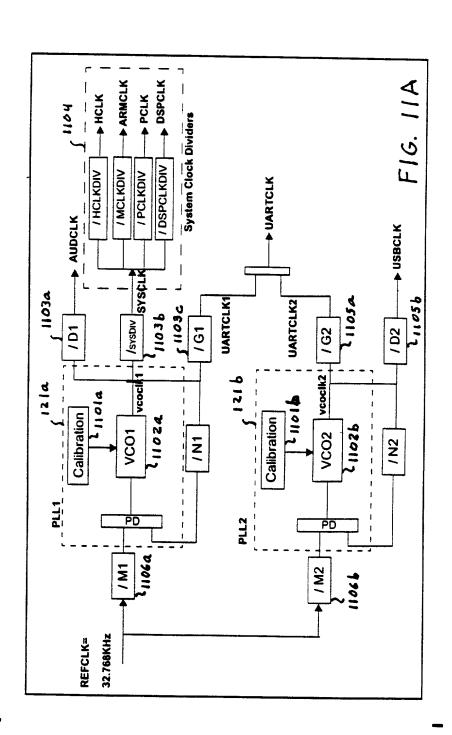
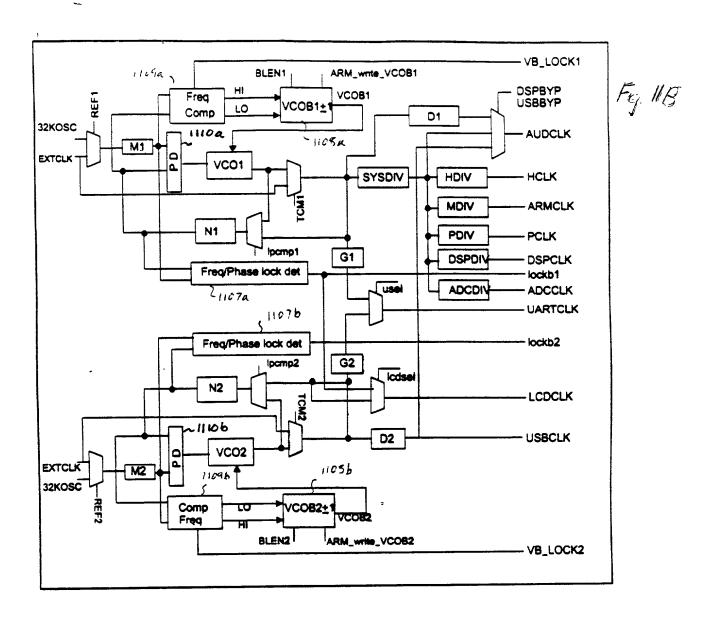
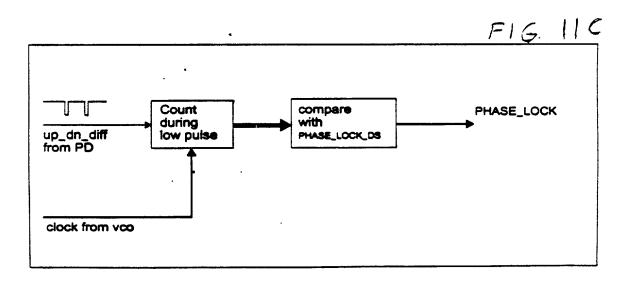


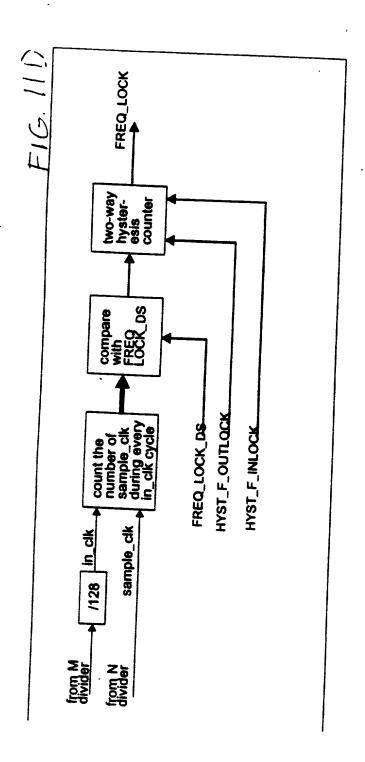
FIG. 10B





:





•

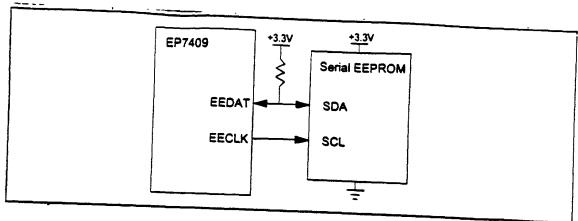
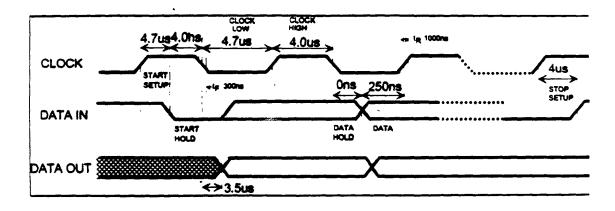
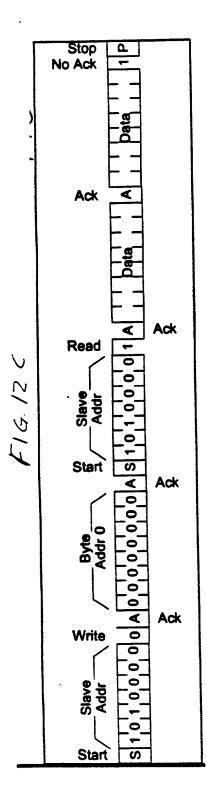
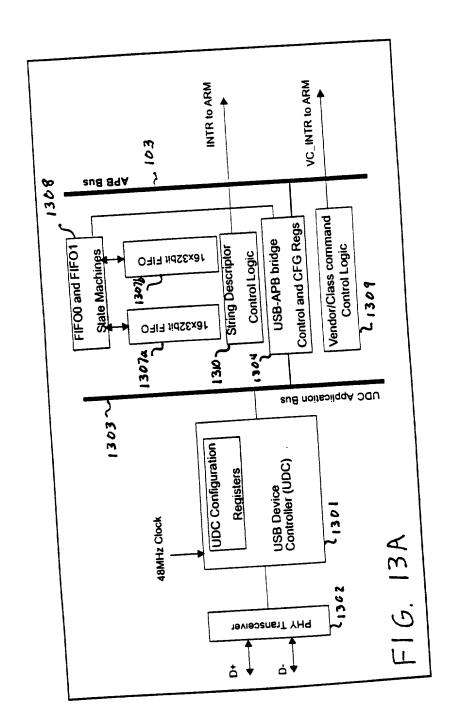


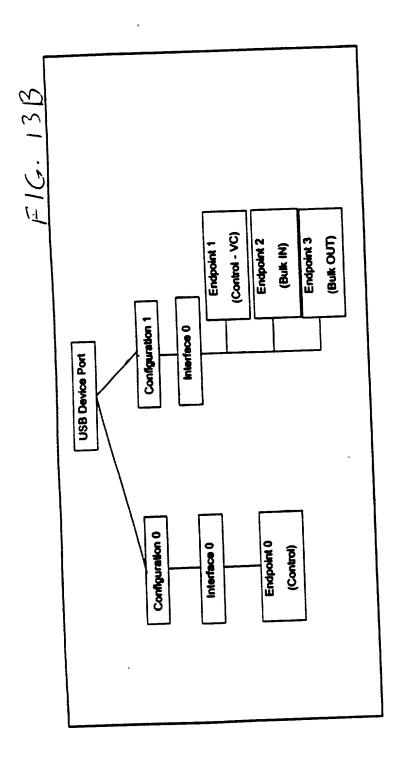
Fig. 12 A

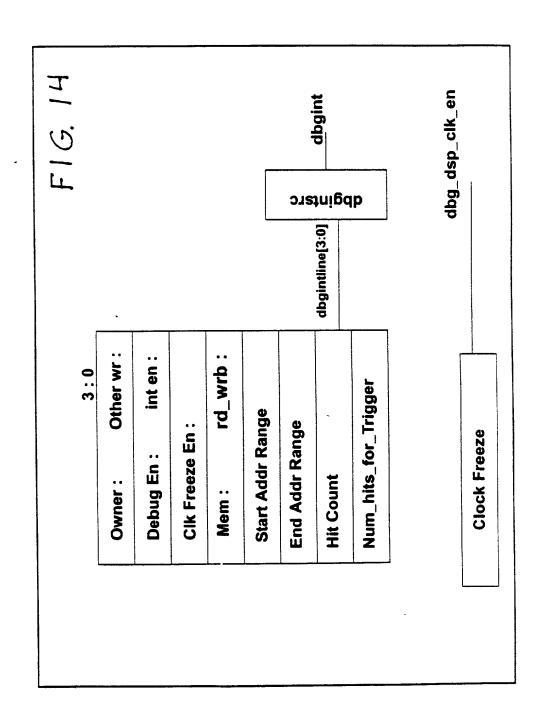
Fig 12B

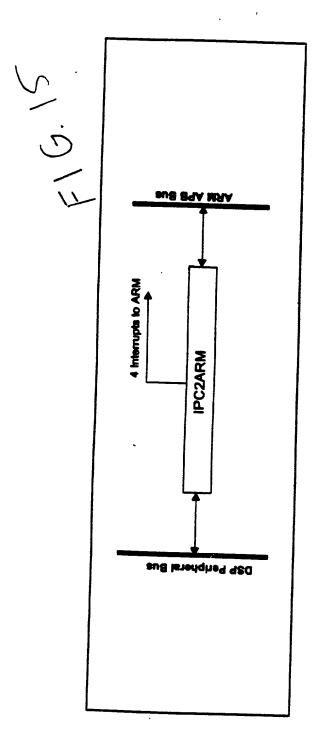


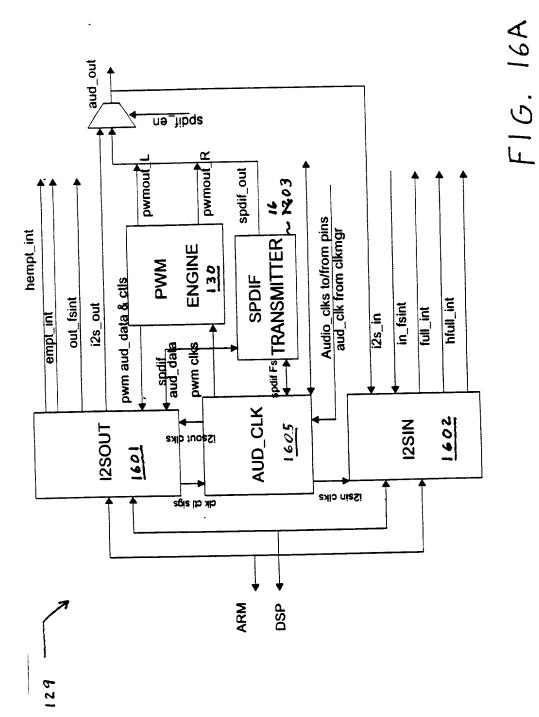


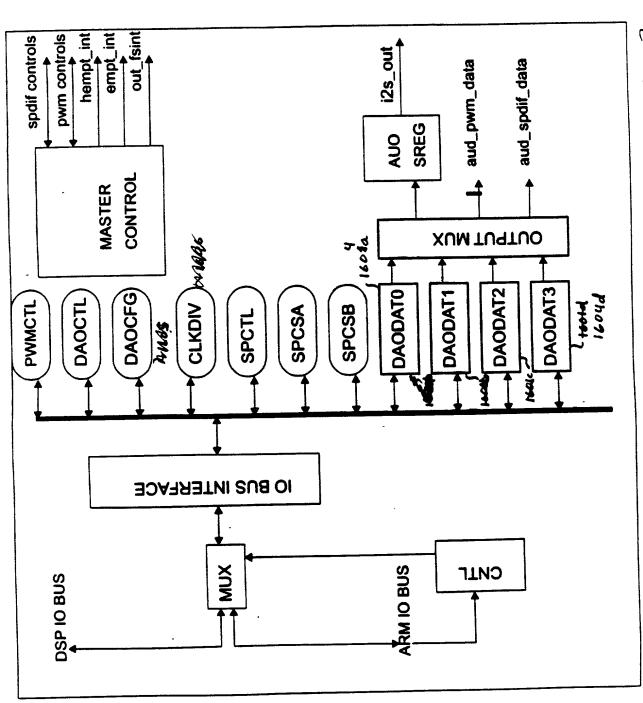




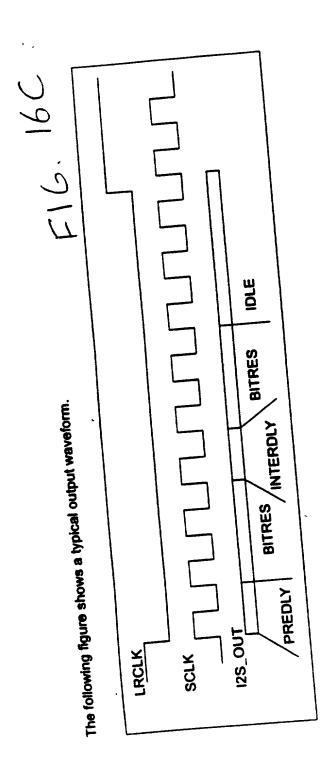




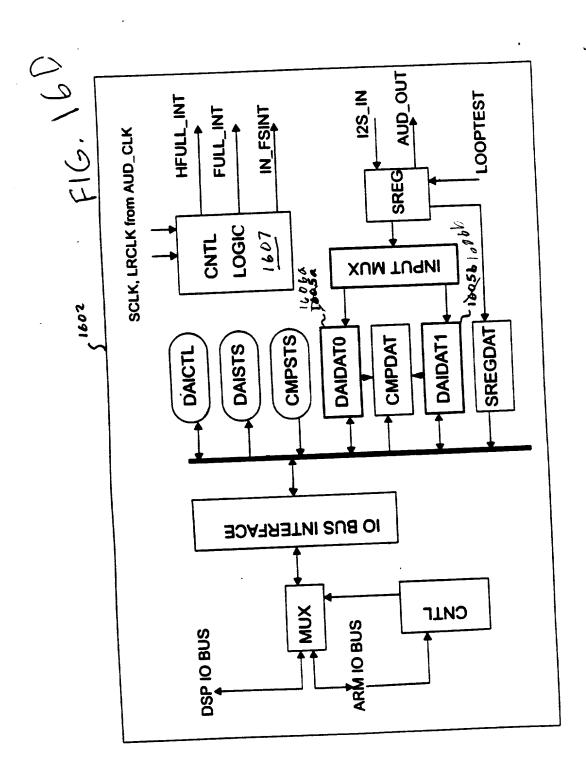


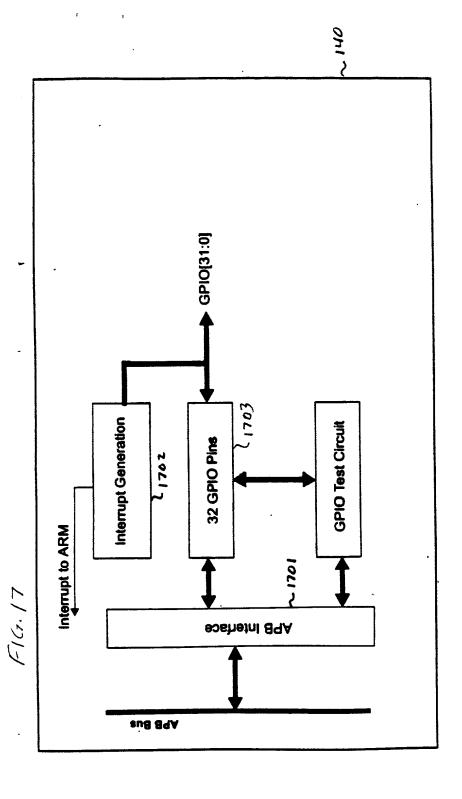


F16. 16B

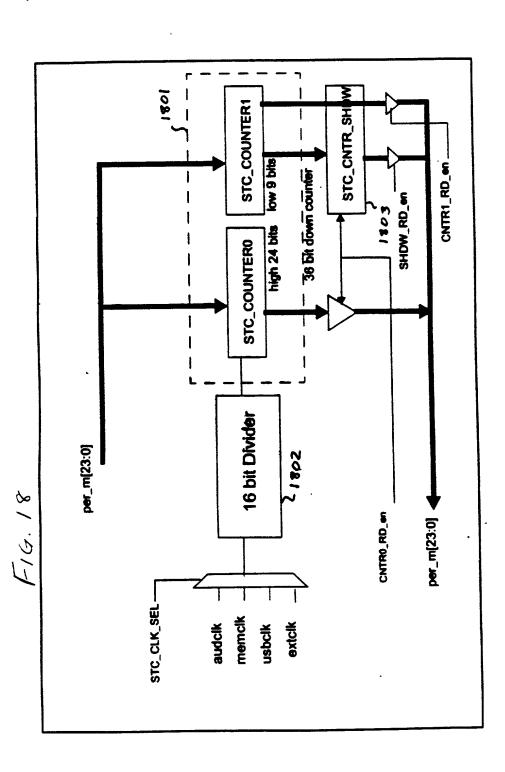


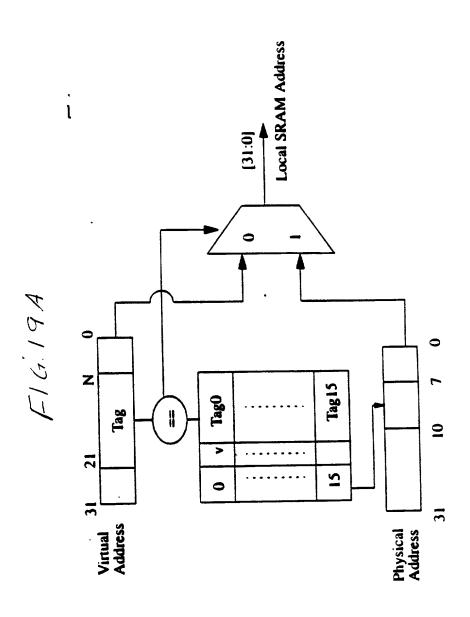
٠.**:**



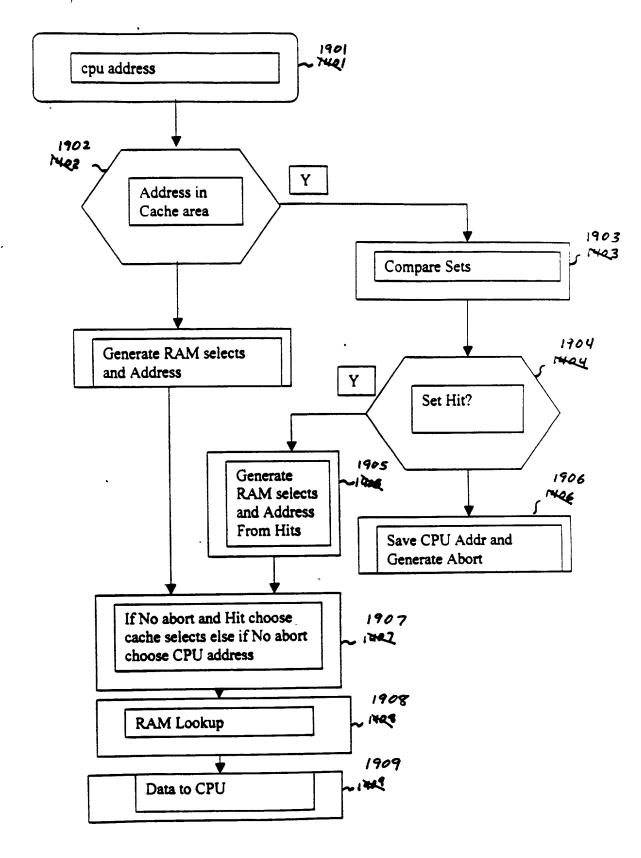


.





F16 19B



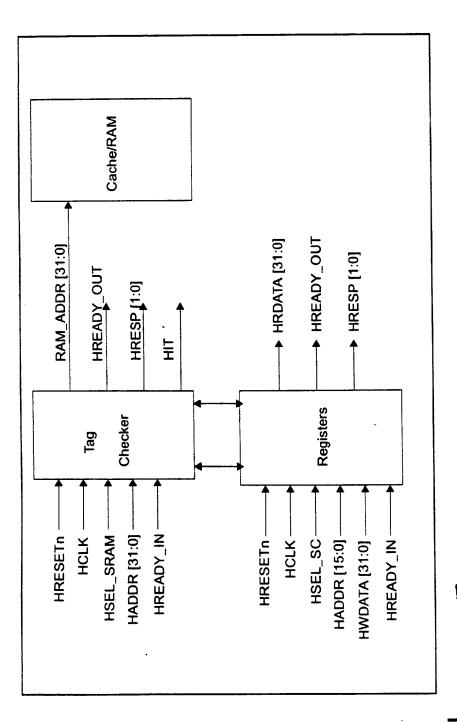


FIG. 19C

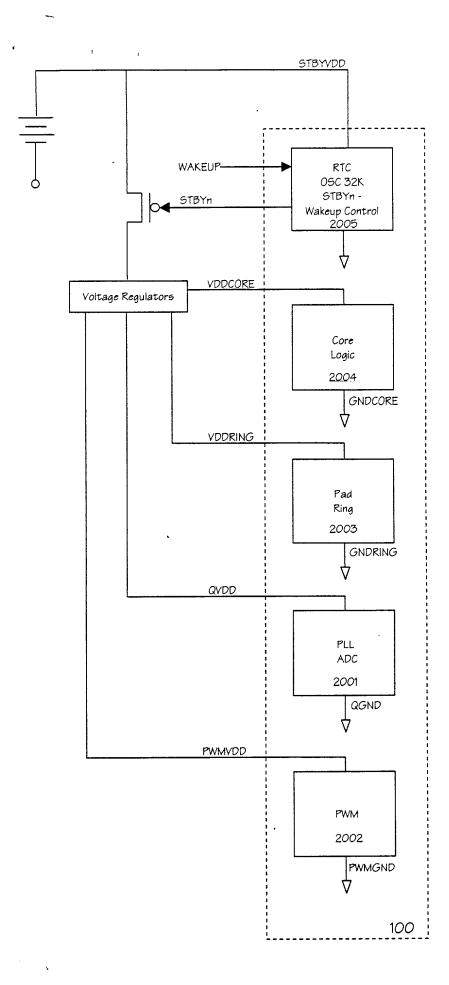


Fig. 20A

